

IN THE CLAIMS:

1. (cancelled)

2-5. (previously cancelled)

6. (new) A data processor on a semiconductor substrate, comprising:

an electrically erasable or programmable non-volatile program memory;

a central processing unit capable of accessing the non-volatile program memory; and

a control circuit terminating a process for erasing or programming of data in the program memory in response to an interrupt request or an exception processing request to the central processing unit during erasing or programming of the data in the program memory in accordance with a request for erasing or programming the data in the program memory.

7. (new) A data processor formed on a semiconductor substrate, comprising:

an electrically erasable or programmable non-volatile program memory;

a central processing unit capable of accessing the non-volatile program memory; and

a control circuit excluding an interrupt request or an exception processing request to the central processing unit during erasing or programming of data in the non-volatile program memory in accordance with a request for erasing or programming the data in the non-volatile program memory.